



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Anthony J. Benson  
Assignee: Hewlett-Packard Company  
Title: CIRCUIT FOR SWITCHING ONE OR MORE HVD  
TRANSCEIVERS  
Serial No.: 09/811,192 Filing Date: March 17, 2001  
Examiner: Cho, James H Group Art Unit: 2819  
Docket No.: 10003439-1 Confirmation No.: 6675

June 13, 2005

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

This paper is responsive to the Notice of Allowability dated March 15, 2005.

The Examiner's statement of reasons for allowance should be amended to read:

"Although Depta discloses a circuit for protection of low-voltage differential device from excessively high signal voltages, one of ordinary skill in the art would not have been motivated to modify the teach of Depta to further include, among other things, the specifics of a comparator comprising a comparator output configured as an open collector, a pullup resistor having two terminals where one terminal is connected to the comparator output and the other connected to the voltage source, and a programmable logic device connected to the terminal of the pullup resistor connected to the comparator output as required by claims 1 and 12.

Noted  
1/24/06

"Claim 13 is allowable on the basis that none of the prior art disclose or fairly suggest a method for controlling a device connected to a bus comprising checking for a diffsense signal from the bus, comparing the diffsense signal to a reference voltage, generating a diffsense prime signal based on said comparing, selectively modifying said generated diffsense prime signal, transmitting said selectively modified diffsense prime signal to the device, and switching the device off in response to asserting said diffsense prime signal low.

"Claims 16-18 are allowable on the basis that none of the prior art disclose or fairly suggest a method for controlling a device comprising determining whether a diffsense signal on a bus is within a specified diffsense voltage range associated with a differential bus, generating a diffsense prime signal based on the determination, selectively modifying the diffsense prime signal, and selectively activating and deactivating the device based on the modified diffsense prime signal."

I hereby certify that this correspondence is being facsimile transmitted to the USPTO, on the date shown below:

Donna M Kraft  
(Signature)

Donna M Kraft  
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Respectfully submitted,

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